

Full-size CPU Card

FSB-866G

FSB-866G

Intel®Pentium® 4/Celeron®-D

PICMG, Full-size CPU Card

With DDR, Ethernet,

CompactFlash™

FSB-866G Rev. A Manual 1st Ed.

Aug. 2005

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Packing List

Before you begin installing your card, please make sure that the following materials have been shipped:

- 1 FSB-866G CPU Card
- 1 Floppy Cable
- 1 ATA-100 Cable
- 1 USB Cable
- 1 Keyboard Cable
- 1 Serial + Parallel Cable
- 1 Serial Cable
- 1 ATX Cable
- 4 Serial ATA Cable
- 1 CPU cooler. Fan for LGA 775 Pentium® 4
- 1 Quick Installation Guide
- 1 CD-ROM for manual (in PDF format) and drivers

If any of these items should be missing or damaged, please contact your distributor or sales representative immediately.

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Chapter

1

**General
Information**

1.1 Introduction

The FSB-866G utilizes Intel® LGA 775 Pentium® 4 CPUs with high CPU core frequencies up to 3.8GHz with the Front Side Bus (FSB) running at 533 to 800MHz. Moreover, this model supports dual channel DDR memory (DDR333/400) providing a maximum capacity up to 2GB. The VGA controller of FSB-866G is an integrated GMA900 2D/3D Graphics Accelerator inside the Intel 915GV chipset which enhances VGA performance capability, with a core frequency up to 333MHz. The chipset is in Intel's embedded roadmap that helps ensure this model's continued production.

In addition to the powerful computing engine, the FSB-866G comes with seven USB 2.0 ports, one Keyboard and Mouse connector, one standard FDD port, one IrDA header, CompactFlash™ Type II socket and Ultra ATA100 & SATA channels. This full-function design makes the FSB-866G a cost-effective industrial CPU card, ideal for system integrators.

The FSB-866G is also equipped with a PCI-Express LAN control IC, Marvell 88E8036/88E8053, for supporting 2 10/100 Mb or full-speed Giga LANs. This flexible feature provides networking access abilities for high-speed applications such as gateway, VPN, and Mini-server.

The AAEON FSB-866G full-size CPU card is designed to fulfill the requirements of tough applications that demand high speed and stable computing performance. We strongly recommend you to adopt this leading-edge model – high performance FSB-866G as one of your integration solutions.

1.2 Features

- Support Intel® Pentium® 4 / Celeron®-D CPU up to 3.8GHz
- DDR 333/400 memory support up to 2GB
- Integrated GMA900 2D/3D Graphics Accelerator, VGA supports
- Integrated AC-97 Codec Audio (Daughter Board optional)
- 2 PCI-express Connectors Can Support 2 Full-speed Gigabit LANs
- Supports 4 Full-speed Gigabit LANs (optional)
- Supports Ultra ATA100 & SATA & CompactFlash™ Type-II Storage
- 4 SATA Connectors support 4 SATA devices
- 1 IDE Slot supports 2 IDE devices
- Adding ITE8211 up can support 3 IDE slots (optional)
- 7 USB2.0/ 1 RS-232, 1 RS-232/422/485/ 1 Parallel/ 1 IrDA Port
- Watchdog Function 1~255 sec.

1.3 Specification

System

- CPU: Support Intel® LGA 755 Pentium® 4 and Celeron®-D Processors up to 3.8 GHz (533/800MHz FSB)
- Chipset: Intel® 915GV + Intel® 82801FB/FR (ICH6/ICH6R)
- Ethernet: PCI Express 10/100Mb or 10/100/1000Mb LAN optional, RJ-45 x 2 Marvell 88E8036/88E8053 controller
- System Memory: 184-pin 2.5V DDR DIMM Socket x 2, total up to 2GB Support Dual Channel DDR memory (DDR 333/400)
- BIOS: Award Plug & Play FWH BIOS – 4Mb ROM
- Watchdog Timer: 1~255 step, can be set with software on Super I/O
- SSD: Supports CompactFlash™ type II connector, USB to IDE

- Expansion Interface: ISA/ PCI/ PCI Express Connector (DF13-30P)
- Battery: Lithium battery
- Power Supply Voltage: +12V ATX, other supply from Backplane
- VGA Controller: Integrated GMA 900 on Intel® 915GV, Core frequency up to 333MHz
- Audio (Daughter board): Realtek ALC655 AC97 Codec, MIC-in/ Line-in/ Line-out/ CD-IN
- IDE Interface: ATA-100 x 1 channel (Support two ATAPI devices); SATA-150 x 4 (Support four SATA devices)
- Universal Serial Bus: USB 2.0 Port x 7; 5 x 2 pin header for internal x 3; Type-A connector onboard x 1
- ISA Interface: PCI to ISA Bridge Used
- IR Interface: Supports IrDA header x 1
- RTC: Internal RTC
- Operation Temp.: 32°F~140°F (0°C ~60°C)

I/O

- FDD Interface: Standard FDD port x 1
(supports up to 1 floppy device)
- Serial Ports: Two COM ports: (Internal pin header, external D-sub x2)
COM 1: RS-232
COM 2: RS-232/422/485
- Parallel Port: Supports SPP/EPP/ECP mode
- Keyboard & Mouse connector:
Mini-DIN for PS/2 Keyboard and mouse connector x 1;
Internal keyboard pin header x 1
- IrDA: IrDA Tx/Rx header x 1
- USB Three 5 x 2 pin headers
(support 7 USB 2.0 ports)

Chapter

2

Quick Installation Guide

Notice:

The Quick Installation Guide is derived from Chapter 2 of user manual. For other chapters and further installation instructions, please refer to the user manual CD-ROM that came with the product.



2.1 Safety Precautions

Warning!

Always completely disconnect the power cord from your board whenever you are working on it. Do not make connections while the power is on, because a sudden rush of power can damage sensitive electronic components.

Caution!

Always ground yourself to remove any static charge before touching the board. Modern electronic devices are very sensitive to static electric charges. Use a grounding wrist strap at all times. Place all electronic components on a static-dissipative surface or in a static-shielded bag when they are not in the chassis

2.4 List of Jumpers

The board has a number of jumpers that allow you to configure your system to suit your application.

The table below shows the function of each of the board's jumpers:

Jumpers

Label	Function
JP1	Clear CMOS

2.5 List of Connectors

The board has a number of connectors that allow you to configure your system to suit your application. The table below shows the function of each board's connectors:

Connectors

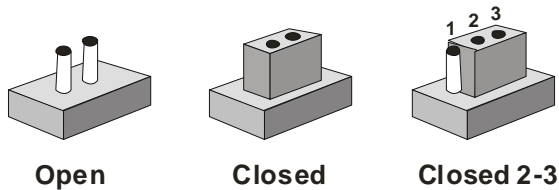
Label	Function
FP1	Front Panel Connector 1
FP2	Front Panel Connector 2
ATX1	ATX Power_12V Connector
ATX2	ATX Power Connector
VGA1	VGA Display Connector
FDD1	Floppy Connector
IDE1~3	EIDE Connector
SATA1~4	Serial ATA Connector
CFD1	Compact Flash Slot
COM1	RS-232 Serial Port Connector
COM2	RS-232/422/485 Serial Port Connector
IR1	IrDA Connector
LPT1	LPT Port Connector
USB1~3	USB Connector
USB4	USB Connector
LAN1	10/100 or 100/1000 Base-Tx Ethernet Connector
LAN2	10/100 or 100/1000 Base-Tx Ethernet Connector
DIMM1~2	DIMM Slot

FAN1	Fan Connector
CN1	ATX Power Control Connector
CN6	AC97 Connector
CN7	PS2 Keyboard/Mouse Connector
CN8	Internal Keyboard Connector
CN2	LAN 1 Active LED Connector
CN4	LAN 2 Active LED Connector
CN9	PCI Express x1 Connector

2.6 Setting Jumpers

You configure your card to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip.

To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2 and 3. In this case you would connect either pins 1 and 2 or 2 and 3.



A pair of needle-nose pliers may be helpful when working with jumpers.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any change.

Generally, you simply need a standard cable to make most connections.

2.7 Clear CMOS (JP1)

JP1	Function
Close	Clear
Open	Protected (default)

2.8 Front Panel Connector (FP1)

Pin	Signal	Pin	Signal
1	Power On Button(+)	2	Reset Switch(+)
3	Power On Button(-)	4	Reset Switch(-)
5	IDE LED(+)	6	Power LED(+)
7	IDE LED(-)	8	Power LED(-)

2.9 Front Panel Connector (FP2)

Pin	Signal	Pin	Signal
1	External Speaker(+)	2	KeyBoard Lock (+)
3	N.C.	4	GND
5	Internal Buzzer(-)	6	I2C Bus SMB Clock
7	External Speaker(-)	8	I2C Bus SMB Data

Pin 5, 7 closed: Internal Buzzer Enable

2.10 RS232 Serial Port Connector (COM1)

Pin	Signal	Pin	Signal
1	DCD	2	RXD
3	TXD	4	DTR
5	GND	6	DSR
7	RTS	8	CTS
9	RI	10	N.C.

2.11 RS232/422/485 Serial Port Connector (COM2)

Pin	Signal	Pin	Signal
1	DCD(422TXD-/485DATA-)	2	RXD(422RXD+)
3	TXD(422TXD+/485DATA+)	4	DTR(422RXD-)
5	GND	6	DSR
7	RTS	8	CTS
9	RI	10	N.C

2.12 IrDA Connector (IR1)

Pin	Signal
1	+5V
2	N.C.
3	IRRX
4	GND
5	IRTX
6	N.C.

2.13 LPT Port Connector (LPT1)

Pin	Signal	Pin	Signal
1	#STROBE	2	#AFD
3	DATA0	4	#ERROR
5	DATA1	6	#INIT
7	DATA2	8	#SLIN
9	DATA3	10	GND
11	DATA4	12	GND
13	DATA5	14	GND
15	DATA6	16	GND
17	DATA7	18	GND
19	#ACK	20	GND
21	BUSY	22	GND
23	PE	24	GND
25	SELECT	26	N.C.

2.14 USB Connector (USB1~3)

Pin	Signal	Pin	Signal
1	+5V	2	GND
3	USBD1-	4	GND
5	USBD1+	6	USBD2+
7	GND	8	USBD2-
9	GND	10	+5V

2.15 Fan Connector (FAN1)

Pin	Signal
1	GND
2	+12V
3	Speed Sense
4	FAN Out Control

2.16 ATX Power Control Connector (CN1)

Pin	Signal
1	PS-ON
2	+5V
3	+5VSB

AT Power Use: Close Pin 2, 3

2.17 LAN LED Connector (CN2, CN4)

Pin	Signal	Pin	Signal
1	Link LED (-)	2	Active LED (+)

2.18 AC97 Connector (CN6)

Pin	Signal	Pin	Signal
1	AC RST-	2	AC SYNC
3	AC_DAIN2	4	AC_DAOUT
5	GND	6	AC_BCLK
7	GND	8	+5V
9	Lock	10	+3.3V

2.19 PS/2 Keyboard/Mouse Connector (CN7)

Pin	Signal
1	KB_DATA
2	MS-DATA
3	GND
4	+5V
5	KB_CLK
6	MS_CLK

2.20 Internal Keyboard Connector (CN8)

Pin	Signal
1	KB_CLK
2	KB_DATA
3	N.C
4	GND
5	+5V

2.21 PCI Express Connector (CN9)

Pin	Signal	Pin	Signal
1	GND	2	+3.3V
3	PCIe-RX1	4	+3.3V
5	PCIe-RX1#	6	PCIe-WAKE#
7	GND	8	PCIe-RST#
9	PCIe-CLK1	10	+3.3VSB
11	PCIe-CLK1#	12	+3.3VSB

13	GND	14	PCle_TX2
15	PCle_TX1	16	PCle_TX2#
17	PCle_TX1#	18	GND
19	+12V	20	PCle-CLK2
21	+12V	22	PCle-CLK2#
23	SMBDATA	24	GND
25	SMBCLK	26	PCle-RX2
27	+3.3V	28	PCle-RX2#
29	+3.3V	30	GND

Chapter

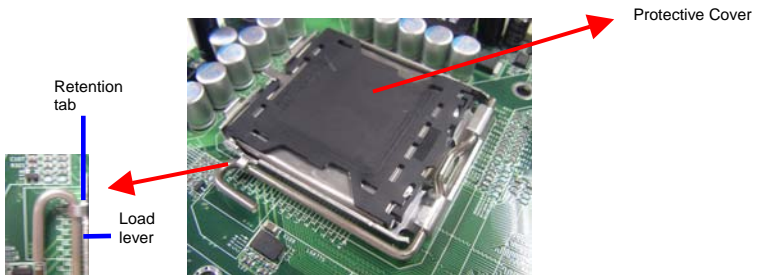
3

CPU, Heatsink and Fan Installation

3.1 CPU installation and motherboard handling

Note: When integrating a Pentium 4 processor-based system, be sure to take the proper electrostatic discharge (ESD) precautions.

Caution: Please follow the steps below to ensure proper installation of your processor. Failure to follow these instructions may result in damage to your CPU and/or your motherboard.



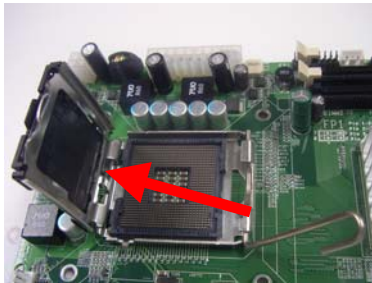
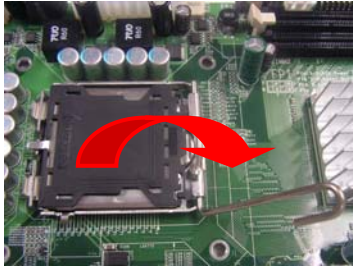
Step 1: (Socket Preparation)

Note: Visually inspect to ensure that the socket protective cover is present and securely fastened.

1. Opening the Socket:

Note: Hold corner of the load plate while opening and closing the load lever. This will prevent the bounce back of the load plate which can cause bent contacts.

- a. Disengage the Load Lever by pressing down and out on the hook allowing the lever to clear the Retention Tab and rotate to the fully open position.



- b. Rotate Load Plate to fully open position.
- c. Remove the Protective Cover

Caution: Do not touch the Socket Contacts. This may result in damage to the contacts.

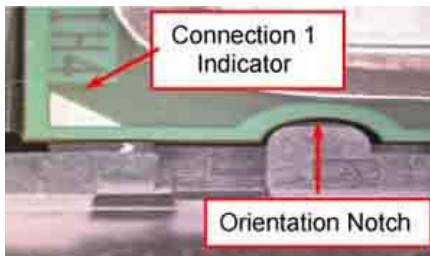
Note: Visually inspect the contact area for bent contacts and foreign material.

Step 2: (Processor Handling)

Note: Handle Processor by substrate edges only.

Caution: Do not touch processor contacts at any time

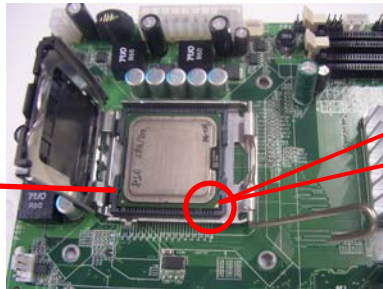
- a. Remove processor from shipping media by grasping substrate edges only
- b. Visually inspect the processor gold pad array for presence of foreign material. If foreign material is present the pads may be wiped clean with a lint-free cloth and isopropyl alcohol.
- c. Locate the connector 1 indicator and the two orientation notches. (See photo)



Step 3: (Positioning the CPU into socket)

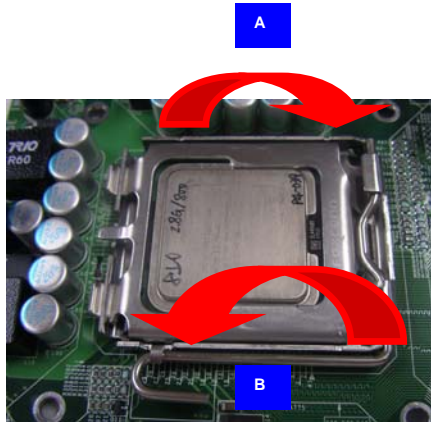
- a. Grasp the processor with your thumb and forefinger on the edges with the orientation notches. The socket has cut-outs for your fingers when handled in this orientation.
- b. Carefully place the CPU into the socket with a purely vertical motion.
- c. Verify that the CPU is in the socket body and properly mated to the orientation keys.

The **Alignment Key** is for you to check if the CPU is aligned with the CPU notch properly.



**Yellow
Triangle
Mark**

- d. Close the upper plate (A), place the load lever back to the original position and tightly wedge the lever into the fastening tab. (B)



Step 4: (Heatsink/ Fan Installation).

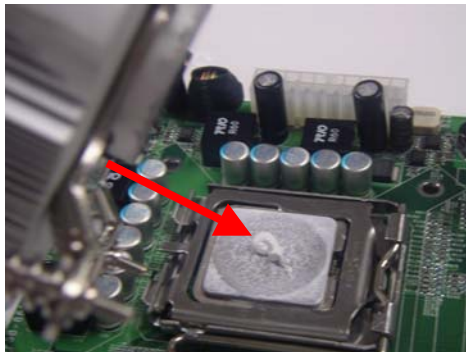
- a. Place bottom bracket underneath the socket making sure that threaded socket is aligned with the holes in the motherboard.



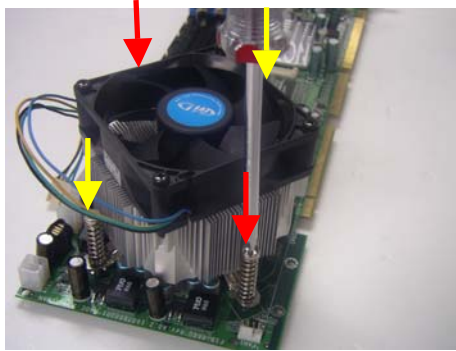
- b. Viewing the board again from the topside make sure the bracket on the solder side has not shifted from its position. Daub thermal compound on the CPU.



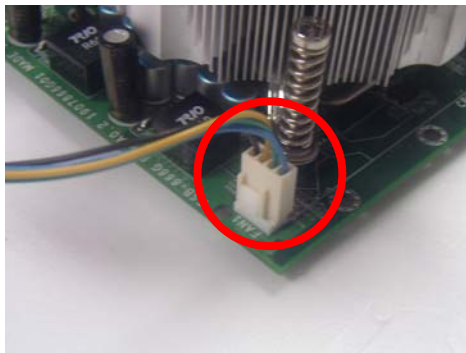
- c. Place the Heatsink on the top of CPU. Please make sure that the four screws are aligned with the holes on the board.



- d. Fasten the four screws. First fasten the two screws (yellow) and then fasten the two screws (red) in sequence to secure the heatsink in place.



- e. Connect the CPU fan cable to the power connector labeled FAN1 on the board.



Chapter

4

**Award
BIOS Setup**

4.1 System Test and Initialization

These routines test and initialize board hardware. If the routines encounter an error during the tests, you will either hear a few short beeps or see an error message on the screen. There are two kinds of errors: fatal and non-fatal. The system can usually continue the boot up sequence with non-fatal errors. Non-fatal error messages usually appear on the screen along with the following instructions:

Press <F1> to RESUME

Write down the message and press the F1 key to continue the boot up sequence.

System configuration verification

These routines check the current system configuration against the values stored in the CMOS memory. If they do not match, the program outputs an error message. You will then need to run the BIOS setup program to set the configuration information in memory.

There are three situations in which you will need to change the CMOS settings:

1. You are starting your system for the first time
2. You have changed the hardware attached to your system
3. The CMOS memory has lost power and the configuration information has been erased.

The FSB-866G CMOS memory has an integral lithium battery backup for data retention. However, you will need to replace the complete unit when it finally runs down.

4.2 Award BIOS Setup

Awards BIOS ROM has a built-in Setup program that allows users to modify the basic system configuration. This type of information is stored in battery-backed CMOS RAM so that it retains the Setup information when the power is turned off.

Entering Setup

Power on the computer and press immediately. This will allow you to enter Setup.

Standard CMOS Features

Use this menu for basic system configuration. (Date, time, IDE, etc.)

Advanced BIOS Features

Use this menu to set the advanced features available on your system.

Advanced Chipset Features

Use this menu to change the values in the chipset registers and optimize your system performance.

Integrated Peripherals

Use this menu to specify your settings for integrated peripherals. (Primary slave, secondary slave, keyboard, mouse etc.)

Power Management Setup

Use this menu to specify your settings for power management. (HDD power down, power on by ring, KB wake up, etc.)

PnP/PCI Configurations

This entry appears if your system supports PnP/PCI.

PC Health Status

This menu allows you to set the shutdown temperature for your system.

Frequency/Voltage Control

Use this menu to specify your settings for auto detect DIMM/PCI clock and spread spectrum.

Load Fail-Safe Defaults

Use this menu to load the BIOS default values for the minimal/stable performance for your system to operate.

Load Optimized Defaults

Use this menu to load the BIOS default values that are factory settings for optimal performance system operations. While AWARD has designated the custom BIOS to maximize performance, the factory has the right to change these defaults to meet their needs.

Set Supervisor/User Password

Use this menu to set Supervisor/User Passwords.

Save and Exit Setup

Save CMOS value changes to CMOS and exit setup.

Exit Without Saving

Abandon all CMOS value changes and exit setup.

You can refer to the "AAEON BIOS Item Description.pdf" file in the CD for the meaning of each setting in this chapter.

Chapter

5

**Driver
Installation**

The FSB-866G comes with a CD-ROM that contains all drivers your need.

In addition, you can activate the installation items through Autorun program which will install each driver directly. If your system do not support Autorun program or you cannot install drivers successfully, please read instructions below for further detailed installations.

Follow the sequence below to install the drivers:

Step 1 – Install INF Driver

Step 2 – Install VGA Driver

Step 3 – Install LAN Driver

Step 4 – Install AC97 Driver

Optional 1 – Install W83627EHG AP For FSB-866G Driver

Please read following instructions for detailed installations.

5.1 Installation:

Insert the FSB-866G CD-ROM into the CD-ROM Drive. And install the drivers from Step 1 to Step 4 in order.

Step 1 – Install INF Driver

1. Click on the **Step 1 –INF** folder.
2. Click on the **infinst_auto 1** folder and then double click on the **Setup**.
3. Follow the instructions that the window shows.
4. The system will help you install the driver automatically.
5. Please re-start your computer.

Step 2 – Install VGA Driver

1. Click on the **Step 2 –VGA** folder.
2. Choose the OS your system is.
3. Double click on the **Setup** located in each OS folder.
4. Follow the instructions that the window shows.
5. The system will help you install the driver automatically.
6. Please re-start your computer.

Step 3 – Install LAN Driver

1. Click on the **Step 3 --LAN** folder.
2. Click on the **Yukon_EC_PLUS_04_08** folder.
3. Choose the OS your system is.
4. Follow the instructions that the window shows.
5. The system will help you install the driver automatically.

6. Please re-start computer.

Step 4 – Install AC97 Driver

1. Click on the **Step 4--AC97** folder.
2. Choose the OS your system is.
3. Double click on the **.exe** file located in each OS folder.
4. Follow the instructions that the window shows.
5. The system will help you install the driver automatically.
6. Please re-start your computer.

Optional 1 –Install W83627EHF AP For FSB-866G Driver

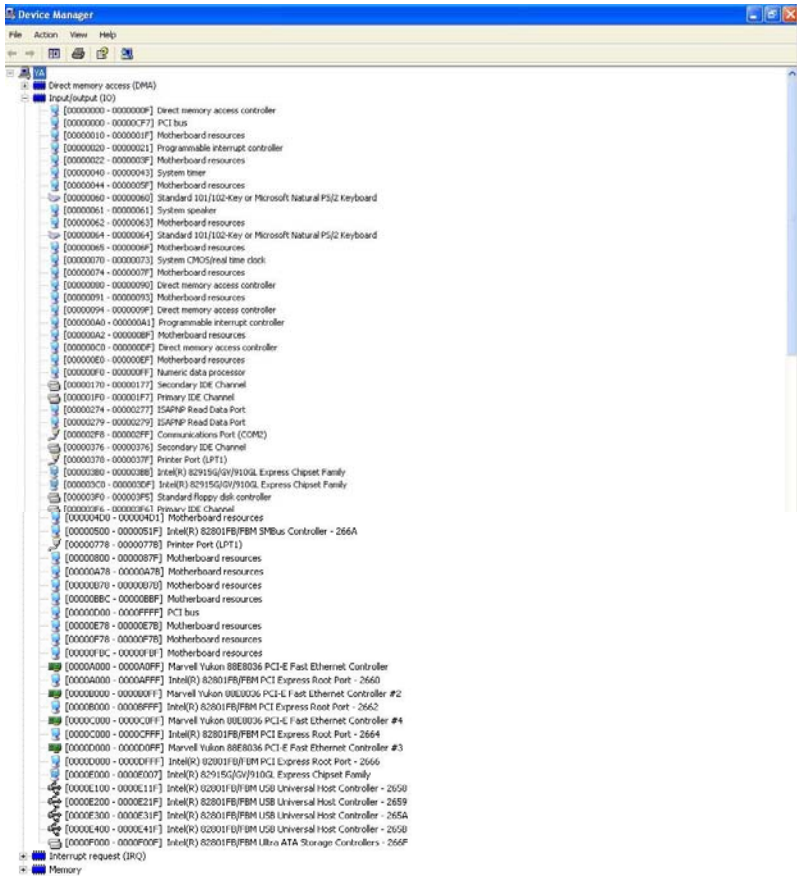
1. Click on the **W83627EHG AP For FSB-866G** folder.
2. Double click on the **Setup**.
3. Follow the instructions that the window shows.
4. The system will help you to install the driver automatically.
5. Please re-start your computer.

Appendix

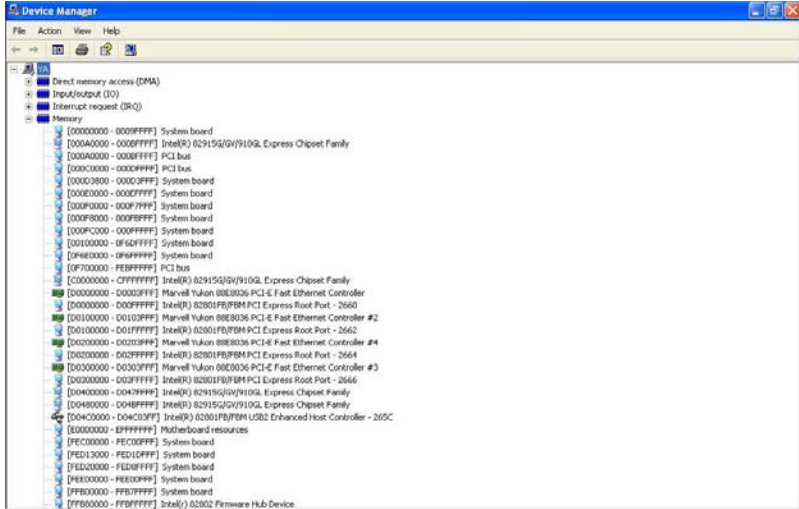
A

I/O Information

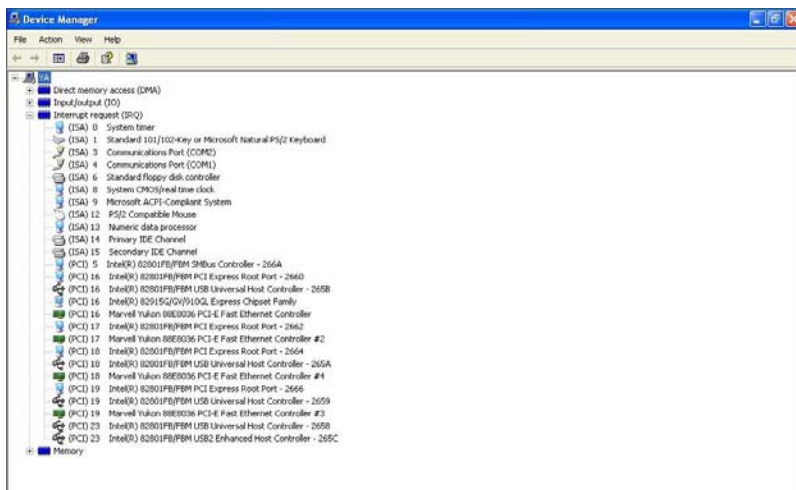
A.1 I/O Address Map



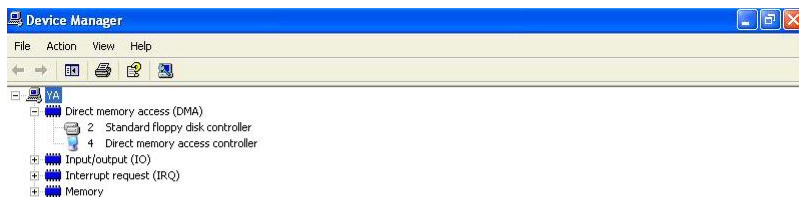
A.2 1st MB Memory Address Map



A.3 IRQ Mapping Chart



A.4 DMA Channel Assignments



Appendix

B

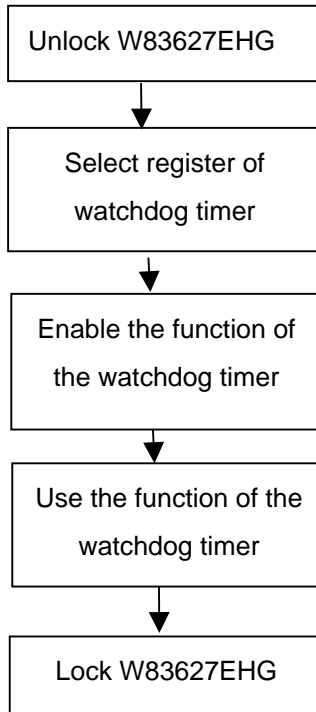
Programming the Watchdog Timer

B.1 Programming

FSB-866G Utilizes W83627EHG chipset as its watchdog timer controller.

Below are the procedures to complete its configuration and the AAEON initial watchdog timer program is also attached based on which you can develop customized program to fit your application.

Configuring Sequence Description



There are three steps to complete the configuration setup:

- (1) Enter the W83627EHG config Mode

- (2) Modify the data of configuration registers
- (3) Exit the W83627EHG config Mode. Undesired result may occur if the config Mode is not exited normally.

(1) Enter the W83627EHG config Mode

To enter the W83627EHG config Mode, two special I/O write operations are to be performed during Wait for Key state. To ensure the initial state of the key-check logic, it is necessary to perform two write operations to the Special Address port (2EH). The different enter keys are provided to select configuration ports (2Eh/2Fh) of the next step.

	Address Port	Data Port
87h,87h:	2Eh	2Fh

(2) Modify the Data of the Registers

All configuration registers can be accessed after entering the config Mode. Before accessing a selected register, the content of Index 07h must be changed to the LDN to which the register belongs, except some Global registers.

(3) Exit the W83627EHG config Mode

The exit key is provided to select configuration ports (2Eh/2Fh) of the next step.

	Address Port	Data Port
0aah:	2Eh	2Fh

WatchDog Timer Register I (Index=F5h, Default=00h)

CRF5 (PLED mode register. Default 0 x 00)

Bit 7-6 : select PLED mode

= 00 Power LED pin is tri-stated.

= 01 Power LED pin is driven low.

= 10 Power LED pin is a 1Hz toggle pulse with 50 duty cycle.

= 11 Power LED pin is a 1/4Hz toggle pulse with 50 duty cycle.

Bit 5-4 : Reserved

Bit 3 : select WDTO count mode.

= 0 second

= 1 minute

Bit 2 : Enable the rising edge of keyboard Reset (P20) to force Time-out event.

= 0 Disable

= 1 Enable

Bit 1-0 : Reserved

WatchDog Timer Register II (Index=F6h, Default=00h)

Bit 7-0 = 0 x 00 Time-out Disable

= 0 x 01 Time-out occurs after 1
second/minute

= 0 x 02 Time-out occurs after 2
second/minutes

= 0 x 03 Time-out occurs after 3
second/minutes

.....

= 0 x FF Time-out occurs after 255

second/minutes

WatchDog Timer Register III (Index=F7h, Default=00h)

- Bit 7** : Mouse interrupt reset Enable or Disable
= 1 Watchdog Timer is reset upon a Mouse interrupt
= 0 Watchdog Timer is not affected by Mouse interrupt
- Bit 6** : Keyboard interrupt reset Enable or Disable
= 1 Watchdog Timer is reset upon a Keyboard interrupt
= 0 Watchdog Timer is not affected by Keyboard interrupt
- Bit 5** : Force Watchdog Timer Time-out. Write Only
= 1 Force Watchdog Timer time-out event: this bit is self-clearing
- Bit 4** : Watchdog Timer Status. R/W
= 1 Watchdog Timer time-out occurred
= 0 Watchdog Timer counting
- Bit 3-0** : These bits select IRQ resource for Watchdog. Setting of 2 selects SMI.

B.2 W83627EHG Watchdog Timer Initial Program

Example: Setting 10 sec. as Watchdog timeout interval

```
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
```

```
Mov dx,2eh          ;Enter W83627EHG config mode
```

```
Mov al,87h         (out 87h to 2eh twice)
```

```
Out dx,al
```

```
Out dx,al
```

```
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
```

```
Mov al,07h
```

```
Out dx,al
```

```
Inc dx
```

```
Mov al,08h         ;Select Logical Device 8 (GPIO Port  
2)
```

```
Out dx,al
```

```
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
```

```
Dec dx
```

```
Mov al,30h         ;CR30 (GP20~GP27)
```

```
Out dx,al
```

```
Inc dx
```

```
Mov al,01h         ;Activate GPIO2
```

```
Out dx,al
```

```
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
```

```
Dec dx
```

```
Mov al,0f5h ;CRF5 (PLED mode register)
```

```
Out dx,al
```

```
Inc dx
```

```
In al,dx
```

```
And al,not 08h ;Set second as counting unit
```

```
Out dx,al
```

```
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
```

```
Dec dx
```

```
Mov al,0f6h ; CRF6
```

```
Out dx,al
```

```
Inc dx
```

```
Mov al,10 ;Set timeout interval as 10 sec.
```

```
Out dx,al
```

```
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
```

```
Dec dx ;Exit W83627EHG config mode
```

```
Mov al,0aah (out 0aah to 2eh once)
```

```
Out dx,al
```

```
;/;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;
```